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EEE 3342C Digital Systems. Summer, 2009. Lab Lab Manual: Lab Introduction. Lab 5. Lab 6. Lab 7. Lab 9. Pin out for common chips (Lab 2) Verilog Reference

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Digital Systems Design. Digital Design by M. Morris Mano, 4 th edition; List of Laboratory Exercises (the "lab manual")

We are Data Systems Group at University of Central Florida.

Student Lab Manual A Design Approach for Digital Systems: Principles and Applications, 11th Edition. By Ronald J. Tocci, Neal Widmer, Greg Moss. Published by Prentice

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