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CiteSeerX - Document Details (Isaac Councill, Lee Giles, Pradeep Teregowda): Abstract This paper presents the measurement and characterization of multilayered

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Raphael is the gold standard, 2D and 3D resistance, capacitance and inductance extraction tool for optimizing on-chip parasitic for multi-level interconnect

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Well-calibrated simulation tools are essential yet good technical content. The extended family of the TCAD group electrostatic simulations in Raphael

Simulation of Semiconductor we have focused on the optimization of low power operation SRAM circuit for 32 nm node with TCAD optimizing the

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