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CiteSeerX - Document Details (Isaac Council, Lee Giles, Pradeep Teregowda): Abstract This paper presents the measurement and characterization of multilayered

Simulation of Semiconductor we have focused on the optimization of low power operation SRAM circuit for 32 nm node with TCAD optimizing the

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Modeling of Parasitic Source/Drain Resistance in We show excellent agreement between our model and simulation which is conducted by Raphael, TCAD Raphael User

Well-calibrated simulation tools are essential yet good technical content. The extended family of the TCAD group electrostatic simulations in Raphael

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